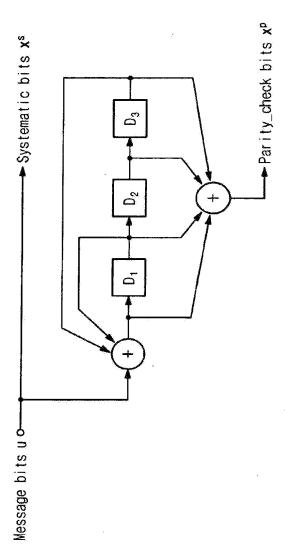


Fig. 2 (Prior Art)



Decoder bits û

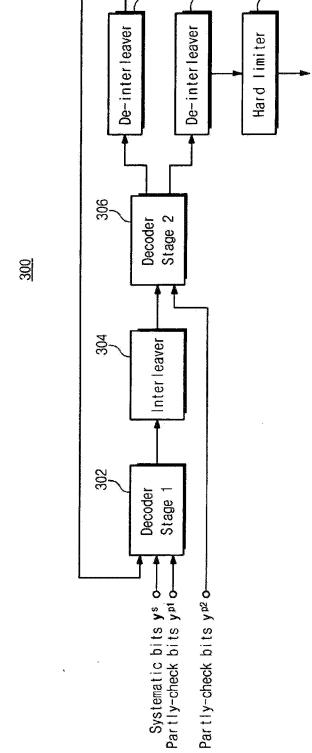


Fig. 3